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16. Addressing apparatus for addressing at least one cell defined by the intersection of respective address electrodes in respective arrays of X and Y dimension address electrodes in an ac plasma panel, said addressing apparatus comprising:

means for applying a high level pulse of one polarity to an address electrode of one dimension array;

means for selecting whether to maintain the high level of one polarity at said address electrode or to bring the electrode to a low level of said one polarity in accordance with desired information to be entered into the plasma panel; and

means for applying a high level pulse of opposite polarity to a respective address electrode of the other dimension array after a high level of one polarity has been selected at said address electrode of said one dimension array, for discharging the defined address cell and entering the desired information into the plasma panel.

17. Addressing apparatus according to claim 16, including means for applying a second high level pulse of said one polarity to said address electrode of said one dimension array after the end of said high level pulse of opposite polarity for enabling the controllable discharging of said address electrode from said high level to said low level of said one polarity.

18. Addressing apparatus for addressing at least one cell defined by the intersection of respective address electrodes in respective arrays of X and Y dimension address electrodes in an ac plasma panel, said addressing apparatus comprising:

means for charging an address electrode of one dimension array to a high level of one polarity;

means for selecting whether to maintain the high level of one polarity at said address electrode or to bring the electrode to a low level of said one polarity in accordance with desired information to be entered into the plasma panel; and

means for applying a high level of opposite polarity charge to a respective address electrode of the other dimension array after a high level of one polarity has been selected at said address electrode of said one dimension array, for discharging the defined address cell and entering the desired information into the plasma panel.

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19. Addressing apparatus according to claim 18, wherein said means for charging the address electrode of one dimension array includes means for applying a high level pulse of said one polarity to said address electrode.

20. Addressing apparatus according to claim 18, including means for applying a high level pulse of said one polarity to said address electrode of one dimension array after entering said desired information into the plasma panel for enabling the controllable discharging of said address electrode from said high level to said low level of said one polarity.

21. Addressing apparatus according to claim 20, wherein said means for charging the address electrode of one dimension array includes means for applying a high level pulse of said one polarity to said address electrode.

22. A method of addressing at least one address cell defined by the intersection of respective address electrodes in respective arrays of X and Y dimension address electrodes in an ac plasma panel, said method of addressing comprising the steps of:

charging an address electrode of one dimension array to a high level of one polarity;

selecting whether to maintain the charged electrode at the high level or to bring the electrode to a low level of said one polarity in accordance with desired information to be entered into the plasma panel; and

applying a high level of opposite polarity charge to a respective address electrode of the other dimension array for discharging the defined address cell where the charged electrode is maintained at the high level and entering the desired information into the plasma panel.

23. The method of claim 22, wherein said charging includes applying a high level pulse of said one polarity to said address electrode of one dimension array.

24. The method of claim 22, including the further step of applying a high level pulse of said one polarity to said address electrode of one dimension array after entering said desired information into the plasma panel for enabling the controllable discharging of said address electrode from said high level to said low level of said one polarity.

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25. The method of claim 24, wherein said charging includes applying a first high level pulse of said one polarity to said address electrode of one dimension array.

26. Addressing apparatus for addressing at least one pixel defined by the intersection of respective address electrodes in respective arrays of X and Y dimension address electrodes in a display panel, said addressing apparatus comprising:

means for applying a high level pulse of one polarity to an address electrode of one dimension array;

means for selecting whether to maintain the high level of one polarity at said address electrode or to bring the electrode to a low level of said one polarity in accordance with desired information to be entered into the display panel; and

means for applying a high level pulse of opposite polarity to a respective address electrode of the other dimension array after a high level of one polarity has been selected at said address electrode of said one dimension array for entering the desired information into the display panel.

27. Addressing apparatus according to claim 26, including means for applying a second high level pulse of said one polarity to said address electrode of said one dimension array after the end of said high level pulse of opposite polarity for enabling the controllable discharging of said address electrode from said high level to said low level of said one polarity.

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28. Addressing apparatus for addressing at least one pixel defined by the intersection of respective address electrodes in respective arrays of X and Y dimension address electrodes in a display panel, said addressing apparatus comprising:

means for charging an address electrode of one dimension array to a high level of one polarity;

means for selecting whether to maintain the high level of one polarity at said address electrode or to bring the electrode to a low level of said one polarity in accordance with desired information to be entered into the display panel; and

means for applying a high level of opposite polarity charge to a respective address electrode of the other dimension array after a high level of one polarity has been selected at said address electrode of said one dimension array for entering the desired information into the display panel.

29. Addressing apparatus according to claim 28, including means for applying a high level pulse of said one polarity to said address electrode of one dimension array after entering said desired information into the display panel for enabling the controllable discharging of said address electrode from said high level to said low level of said one polarity.

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30. A method of addressing at least one address cell defined by the intersection of respective address electrodes in respective arrays of X and Y dimension address electrodes in a display panel, said method of addressing comprising the steps of:

charging an address electrode of one dimension array to a high level of one polarity;

selecting whether to maintain the charged electrode at the high level or to bring the electrode to a low level of said one polarity in accordance with desired information to be entered into the display panel; and

applying a high level of opposite polarity charge to a respective address electrode of the other dimension array for entering the desired information into the display panel.

31. The method of claim 30, including the further step of applying a high level pulse of said one polarity to said address electrode of one dimension array after entering said desired information into the display panel for enabling the controllable discharging of said address electrode from said high level to said low level of said one polarity.

32. A display panel comprising:

an array of X dimension address electrodes;

an intersecting array of Y dimension address electrodes, where intersections between respective X and Y address electrodes define respective display pixels;

address means for applying an addressing signal during an addressing cycle to selected X and Y address electrodes to activate at least one display pixel;

said address means including, means for charging an address electrode of one said X or Y dimension array to a high level of one polarity;

means for selecting whether to maintain the high level of one polarity at said address electrode or to bring the electrode to a low level of said one polarity in accordance with desired information to be entered into the display panel; and

means for applying a high level pulse of opposite polarity to a respective address electrode of the other said X or Y dimension array after a high level of one polarity has been selected at said address electrode of said one X or Y dimension array for entering the desired information into the display panel.

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33. A display panel according to claim 32, including means for enabling controllable discharging of said address electrode from said high level to said low level of said one polarity after entering said desired information into the display panel.

34. An ac plasma panel comprising:
an array of X dimension electrodes;
an intersecting array of Y dimension electrodes with the
intersections between respective X and Y electrodes defining a
gas discharge cell;
address means for applying a signal to selected X and Y
electrodes to discharge at least one gas discharge cell;
said address means including, means for charging an
address electrode of said X or Y dimension array to a high level
of one polarity;
means for selecting whether to maintain the high level
of one polarity at said address electrode or to bring the
electrode to a low level of said one polarity in accordance with
desired information to be entered into the plasma panel; and
means for applying a high level pulse of opposite
polarity to a respective address electrode of the other said X or
Y dimension array after a high level of one polarity has been
selected at said address electrode of said one X or Y dimension
array for discharging said one gas discharge cell and entering
the desired information into the plasma panel.

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35. An ac plasma panel according to claim 34, including
means for enabling controllable discharging of said address
electrode from said high level to said low level of said one
polarity after entering said desired information into the plasma
panel.
